IN THE SPECIFICATION:

Please replace the paragraph beginning at line 5 on page 10 with the following paragraph,

Assuming that CPE 120 operates consistent with ADSL specification, CPE 120 may transmit data using frequency band 270 230 (i.e., 30 - 138 KHZ) on telephone line 127, and the transmit operation may be performed in a known way. During reception, CPE 120 receives a signal which contains DSL data (band 250), any transmissions according to the HPNA specifications (band 270), and voice band 210. The manner in which CPE 120 recovers the data in DSL data channel in accordance with various aspects of the present invention is described below.

Please replace the paragraph beginning at line 18 on page 10 with the following paragraph.

ADC 320 330 may be implemented to generate 12 bit samples at a frequency of 4.416 Mega Samples per Second. ADC 320 330 may be implemented for a voltage swing of about 3 Volts. Gain amplifier 320 receives the output of filter 310 and amplifies the received signal by about 36 Db to generate the input to ADC 320 330. In an embodiment, ADC 320 330 and gain amplifier 320 may be integrated as one unit, and be referred to as a analog front end (AFE). Post processor 350 processes the samples generated by ADC 320 330 to recover the data encoded in the ADSL channel (band 250). ADC 320 330, gain amplifier 320, and post processor 350 may be implemented in a known way.

Please replace the paragraph beginning at line 14 on page 14 with the following paragraph.

PGA 430 may correspond to gain amplifier 320, and may be implemented to amplify the output of LPF 480. In an embodiment, the amplification factor of PGA 430 can be as high as 9 dB. In general, the amplification factors of PGAs 410, 420 and 430 need to be chosen such that the entire voltage range of ADC 320 330 is taken advantage of, and also to avoid saturating any of the other components in the path. PGAs 410, 420 and 430 may be implemented in a known way. The description is continued with example embodiments of HPF 450.

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